

PATENTS
174/304

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants : David Lewis
Application No. : Not Yet Available
Filed : Concurrently Herewith
For : LOGIC DEVICE LOGIC MODULES HAVING
IMPROVED ARITHMETIC CIRCUITRY
Group Art Unit : Not Yet Available
Examiner : Not Yet Available

POWER OF ATTORNEY BY ASSIGNEE

Under the provisions of 37 C.F.R. § 3.71, the undersigned assignee of record, by virtue of an assignment submitted concurrently herewith for recording, of the above-identified patent application, hereby elects to conduct the prosecution of the above-identified patent application to the exclusion of the inventors. The assignee hereby revokes any powers of attorney previously granted and appoints Derek E. Minihane, Reg. No. 39,774, Robert R. Jackson, Reg. No. 26,183, and Jeffrey H. Ingerman, Reg. No. 31,069, its attorneys, with power of substitution, and with power of appointment of associate attorneys, and of revocation of their powers, to prosecute this application and any divisions, continuations in whole or in part, renewals and reissues of the same, and to transact all business in the Patent and Trademark Office connected therewith;

10/24/2003
3:53 PM
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and assignee requests that communications be sent
to:

Robert R. Jackson
c/o Fish & Neave
1251 Avenue of the Americas
New York, New York 10020-1104

and that telephone calls be directed to:

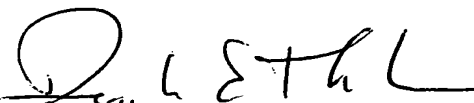
Robert R. Jackson
(212) 596-9000.

Pursuant to 37 C.F.R. § 3.73(b), the undersigned
hereby certifies that the evidentiary documents have been
reviewed and, to the best of assignee's knowledge and
belief, title is in assignee.

The undersigned hereby declares that all
statements made herein of my own knowledge are true and that
all statements made on information and belief are believed
to be true; and further that these statements were made with
the knowledge that willful false statements and the like so
made are punishable by fine or imprisonment, or both, under
Section 1001 of Title 18 of the United States Code and that
such willful false statements may jeopardize the validity of
the application or any patent issued thereon.

ALTERA CORPORATION
ASSIGNEE

11/19/03
Date

By: 
Derek E. Minihaue
Director, Intellectual
Property Law
and Assistant Secretary

DECLARATION
FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

LOGIC DEVICE LOGIC MODULES HAVING IMPROVED ARITHMETIC CIRCUITRY

the specification of which

(check [X] is attached hereto
one)

[] was filed on _____ as
Application Serial No.
and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claim, as amended by any amendment referred to above.

I do not know and do not believe that the invention was ever patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application.

I do not know and do not believe that the invention was in public use or on sale in the United States of America more than one year prior to this application.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known by me to be material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

Send correspondence to:


Robert R. Jackson
FISH & NEAVE
1251 Avenue of the Americas
New York, New York 10020-1104

Direct telephone calls to:

Robert R. Jackson
(212) 596-9000

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful ofalse statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first inventor David Lewis

First Inventor's signature 

Nov 18/03

Date

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